

Summer School

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#keynotespeakers



Marco Di Renzo

Marco Di Renzo (Fellow, IEEE) received the Laurea and Ph.D. degrees in electrical engineering from the University of L'Aquila, Italy, in 2003 and 2007, respectively, and the Habilitation à Diriger des Recherches (Doctor of Science) degree from University Paris-Sud (currently Paris-Saclay University), France, in 2013. Currently, he is a CNRS Research Director (Professor) and the Head of the Intelligent Physical Communications group in the Laboratory of Signals and Systems (L2S) of Paris-Saclay University – CNRS and CentraleSupélec, Paris, France. At Paris-Saclay University, he serves as the Coordinator of the Communications and Networks Research Area of the Laboratory of Excellence DigiCosme, as a Member of the Admission and Evaluation Committee of the Ph.D. School on Information and Communication Technologies, and as a Member of the Evaluation Committee of the Graduate School in Computer Science. He is a Founding Member and the Academic Vice Chair of the Industry Specification Group (ISG) on Reconfigurable Intelligent Surfaces (RIS) within the European Telecommunications Standards Institute (ETSI), where he serves as the Rapporteur for the work item on communication models, channel models, and evaluation methodologies. He is a Fellow of the IEEE, IET, and AAIA; an Ordinary Member of the European Academy of Sciences and Arts, an Ordinary Member of the Academia Europaea; and a Highly Cited Researcher. Also, he holds the 2023 France-Nokia Chair of Excellence in ICT, and was a Fulbright Fellow at City University of New York, USA, a Nokia Foundation Visiting Professor, and a Royal Academy of Engineering Distinguished Visiting Fellow. His recent research awards include the 2021 EURASIP Best Paper Award, the 2022 IEEE COMSOC Outstanding Paper Award, the 2022 Michel Monpetit Prize conferred by the French Academy of Sciences, the 2023 EURASIP Best Paper Award, the 2023 IEEE COMSOC Fred W. Ellersick Prize, and the 2023 IEEE COMSOC Heinrich Hertz Award.