

Summer School

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#keynotespeakers



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Daniel Casini (Member, IEEE) received the degree in embedded computing systems engineering and the master's degree from Scuola Superiore Sant'Anna, Pisa, and the University of Pisa, and the Ph.D. degree (Hons.) in computer engineering from Scuola Superiore Sant'Anna under the supervision of Prof. Alessandro Biondi and Prof. Giorgio Buttazzo. He is an Assistant Professor with the Real-Time Systems (ReTiS) Laboratory, Scuola Superiore Sant'Anna. In 2019, he was a Visiting Scholar with the Max Planck Institute for Software Systems, Germany. His research interests include software predictability in multiprocessor systems, schedulability analysis, synchronization protocols, and the design and implementation of real-time operating systems and hypervisors.